

ΑΝΑΡΤΗΤΕΑ ΣΤΟ ΔΙΑΔΙΚΤΥΟ



ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ
ΥΠΟΥΡΓΕΙΟ ΠΑΙΔΕΙΑΣ, ΕΡΕΥΝΑΣ ΚΑΙ ΘΡΗΣΚΕΥΜΑΤΩΝ
ΓΕΝΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΡΕΥΝΑΣ ΚΑΙ ΤΕΧΝΟΛΟΓΙΑΣ

**ΙΔΡΥΜΑ ΤΕΧΝΟΛΟΓΙΑΣ ΚΑΙ ΕΡΕΥΝΑΣ
ΙΝΣΤΙΤΟΥΤΟ ΠΛΗΡΟΦΟΡΙΚΗΣ**

Ταχ. Διεύθυνση: Ν. Πλαστήρα 100
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Ηράκλειο 11/5/2018

Call for expression of interest for *up to two (2)* positions, one (1) Hardware Design and Test Engineer (full-time) and one (1) Hardware Design and Test Engineer (part-time) in the Institute of Computer Science (ICS) Foundation for Research and Technology – Hellas (FORTH)



Position(s): Two (2) positions for the Ecoscale project

Project: ECOSCALE (GA-671632), funded under H2020 FET-HPC.

Desired starting dates: 1/7/2018.

Duration: 1/7/2018-30/09/2018, 3 months with possibility of extension.

Location: Heraklion, Crete, Greece.

Opening date: 11/5/2018

Closing date: 28/5/2018

Reference: "EcoScale-May2018"

Description:

We seek two experienced members – Hardware Design and Test engineers -for our team, one full time and one *part-time*, with background in European Research Projects. The candidates will participate in the R&D activities of FORTH in the context of the project ECOSCALE: Energy-efficient Heterogeneous COmputing at exaSCALE. In order to reach exascale performance current HPC servers need to be improved. Simple scaling is not a feasible solution due to the increasing utility costs and power consumption limitations. Apart from improvements in implementation technology, what is needed is to refine the HPC application development as well as the architecture of the future HPC systems. ECOSCALE tackles this challenge by proposing a scalable programming environment and hardware architecture tailored. The full time Engineer will work on system bring up and testing in the context of developing the ECOSCALE testbed. The part time Engineer will perform Integration and Evaluation (performance, power) of the ECOSCALE testbed.

Requirements and Desired qualifications

For both positions of Hardware Design and Test engineers, *full time and part-time (60H/month equivalent)*:

- **Requirements:**

MSc degree in Computer Science or Computer Engineering

At least 3 years of documented professional experience in related roles

At least two professional references

Experience with system development projects using based Xilinx FPGA's

- **Desired qualifications:**

Experience with test-driven development and validation

Experience in European Research Projects

Application Submission:

Interested candidates can submit their applications via <http://www.ics.forth.gr/jobs/en/> using the link "[Apply for the position](#)" under the announcement. Applications must include:

- Detailed CV, including qualifications and interests in the above areas, and proof thereof;
- Scanned copies of academic titles;
- List of at least two professional references

For information and questions about the advertised positions, the activities of the group, or the Institute, please contact Nikolaos Papadopoulos (nickpap@ics.forth.gr)

Selection Announcement

The result of the selection will be announced on the website of ICS-FORTH. Candidates have the right to appeal the selection decision, by addressing their written objection to the ICS secretariat within five (5) days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates' scores (ranking of candidates results). All the above information related to the selection procedure will be available at the secretariat of ICS-FORTH in line with the Hellenic Data Protection Authority.